



IN 09/239,898

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

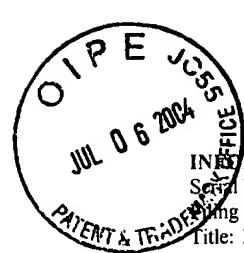
Applicant: Mirmajid Seyyedy et al. Examiner: Guy J. Lamarre
Serial No.: 09/239,898 Group Art Unit: 2133
Filed: January 29, 1999 Docket: 303.550US1
Title: METHOD AND APPARATUS FOR TESTING A MEMORY DEVICE WITH
COMPRESSED DATA USING A SINGLE OUTPUT

INFORMATION DISCLOSURE STATEMENT

MS RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement.



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Dkt: 303.550US1

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

MIRMAJID SEYYEDY ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938
Minneapolis, MN 55402
(612) 373-6973

Date 1 JULY 2004

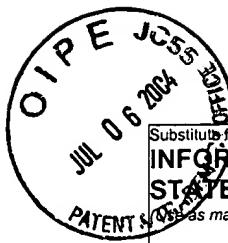
By _____

Robert E. Mates
Reg. No. 35,271

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 1 day of July, 2004.

Name

Signature



Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <small>(use as many sheets as necessary)</small>		Complete if Known Application Number 09/239,898 Filing Date January 29, 1999 First Named Inventor Seyyedy, Mirmajid Group Art Unit 2133 Examiner Name Lamarre, Guy	
Sheet 1 of 1		Attorney Docket No: 303.550US1	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		"16Mb Double Data Rate Synchronous Graphics RAM", IBM Corporation, IBM0616328RL6A,(December 1997),pp. 1-53	
		"16Mbit DDR SGRAM, 128K x 32Bit x 4 Banks Double Data Rate Synchronous Graphic RAM with Bi-directional Data Strobe, Revision 0.6", Samsung Electronics, KM432D5131,(April 1998),pp. 1-48	

EXAMINER

DATE CONSIDERED